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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sidlgata V. Sreenivasan
App. No.: 10/788,700 GPAU: 3765
Filing Date: 02/27/2004 Examiner: Rodney M. Lindsey
Dkt. No.: P107-49-03 Conf. No.: 6995
For: FULL-WAFER OR LARGE AREA IMPRINTING WITH MULTIPLE
SEPARATED SUB-FIELDS FOR HIGH THROUGHPUT LITHOGRAPHY

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☐ Other:

to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed:

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the

Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 502650.

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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mail or Express Mail, in an envelope addressed to the Commissioner for Patents.

Signed: Katrina Prati

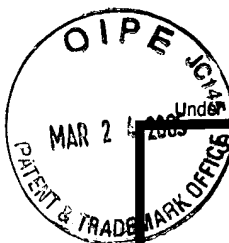
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Date: 3-21-05

Respectfully Submitted,



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STATEMENT BY APPLICANT**

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Sheet 1 of 5

Complete if Known

Application Number	10/788,700
Filing Date	02/27/2004
First Named Inventor	S. V. Sreenivasan
Group Art Unit	3765
Examiner Name	Rodney M. Lindsey
Attorney Docket Number	P107-49-03

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	B1	6,391,217	B2	Schaffer et al.	5/21/2002	
	B2	2002/0177319	A1	Chou	11/28/2002	
	B3	6,713,238	B1	Chou et al.	3/30/2004	
	B4	2004/0065976	A1	Sreenivasan et al.	4/8/2004	
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	B14	6,828,244	B2	Chou	12/7/2004	
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	B16	2004/0197843	A1	Chou et al.	10/7/2004	
	B17	2004/0156108	A1	Chou et al.	8/12/2004	
	B18	2004/0137734	A1	Chou et al.	7/15/2004	
	B19	2004/0131718	A1	Chou et al.	7/8/2004	
	B20	2004/0118809	A1	Chou et al.	6/24/2004	

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**INFORMATION DISCLOSURE
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Sheet 2 of 5

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Application Number	10/788,700
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First Named Inventor	S. V. Sreenivasan
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		Number	Kind Code ² (if known)			
	B21	2004/0046288	A1	Chou	3/11/2004	
	B22	2003/0080472	A1	Chou	5/1/2003	
	B23	2002/0042027	A1	Chou et al.	4/11/2002	
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	B35	5,849,222		Jen et al.	12/15/1998	
	B36	2003/0034329		Chou	2/20/2003	

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Sheet 3 of 5		Application Number 10/788,700		Filing Date 02/27/2004	
		First Named Inventor S. V. Sreenivasan		Group Art Unit 3765	
		Examiner Name Rodney M. Lindsey		Attorney Docket Number P107-49-03	

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		Office 3	Number ⁴	Kind Code ⁵ (if known)				

	B37	WO	2004/013693		Watts et al.	2/12/2004		
	B38	WO	2004/016406		Sreenivasan	2/26/2004		
	B39	WO	00/21689		Chou et al.	4/20/2000		
	B40	JP	02-92603		Kurikawa et al.	4/3/1990		
	B41	JP	02-24848		Kamio	1/26/1990		
	B42	WO	01/47003		Steiner et al.	6/28/2001		
	B43	WO	02/07199		Chou	1/24/2002		
	B44	WO	2004/114016		Chou et al.	12/29/2004		
	B45	WO	99/05724		Chou et al.	2/4/1999		
	B46	WO	03/010289		Chou et al.	2/6/2003		
	B47	WO	03/079416		Chou	9/25/2003		
	B48	WO	03/099536		Chou et al.	12/4/2003		

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				First Named Inventor	S. V. Sreenivasan
				Group Art Unit	3765
				Examiner Name	Rodney M. Lindsey
				Attorney Docket Number	P107-49-03
Sheet	4	of	5		

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

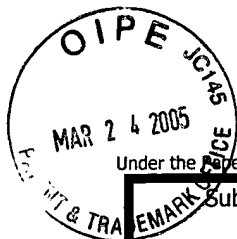
	B49	Chou, Nanoimprint Lithography and Lithographically Induced Self-Assembly, MRS Bulletin, pp. 512 - 517, 7/1/2001	
	B50	Colburn et al., Step and Flash Imprint Lithography for sub-100 nm Patterning, Proceedings of SPIE Vol. 3997, pp. 453 - 457, 1/1/2000	
	B51	Heidari, Nanoimprint Lithography at the 6 in. Wafer Scale, J. Vac. Sci. Technol. B 18(6), pp. 3557 - 3560, 11/1/2000	
	B52	Translation of Japanese Patent 02-92603, 4/3/1990	
	B53	Translation of Japanese Patent 02-24848, 1/26/1990	
	B54	Chou et al., Ultrafast and Direct Imprint of Nanostructures in Silicon, Nature, Vol. 417, (June 2002), pp. 835-837, 6/1/2002	
	B55	Chou et al., Nanoimprint Lithography, Journal of Vacuum Science Technology B 14(16), pp. 4129-4133, 11/1/1996	
	B56	Colburn et al., Development and Advantages of Step-and-Flash Lithography, Solid State Technology, 7/1/2001	
	B57	Colburn et al., Characterization and Modeling of Volumetric and Mechanical Properties for Step and Flash Imprint Lithography Photopolymers, Journal of Vacuum Science Technology. Vol b. 19(6), 11/1/2001	

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**INFORMATION DISCLOSURE
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5

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	B58	Bailey et al., Step and Flash Imprint Lithography: Defect Analysis, Journal of Vacuum Science, B 19(6), pp. 2806-2810, 11/1/2001	
	B59	Bailey et al., Step and Flash Imprint Lithography: Template Surface Treatment and Defect Analysis, Journal of Vacuum Science, B 18(6), pp. 3572-3577, 11/1/2000	
	B60	Schneider et al., Stripes of Partially Fluorinated Alkyl Chains: Dipolar Langmuir Monolayers	

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